



Sheet 1 of 1

FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(37 CFR 1.98(b))Attorney Docket No.:
GR 00 P 1969Applic. No.
09/864,980

Applicant

Peter Aymar, et al.

Filing Date
May 24, 2001Group Art Unit
2133

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
FA	J	0 944 173 A2	9/1999	Europe			
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

FA	O	Biver, et al.: "Architectural Design and Realization of a Single-Chip Viterbi Decoder," 1989, 8220 Integration, the VLSI Journal, Amsterdam, NL, pp. 3-16
	P	

EXAMINER

DATE CONSIDERED

3/27/06

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609;
Draw line through citation if not in conformance and not considered. Include copy of this form with
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